



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

**Tomoaki YABE**

Serial No.: 09/955,131

Filed: September 19, 2001

For: PHASE DETECTOR AND PHASE LOCKED  
LOOP CIRCUIT

Atty. Docket No.: 002372.00031

Group Art Unit: 2634

Examiner: Joseph, J.

Confirmation No.: 8398

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 C.F.R. § 1.97(c)**

U.S. Patent and Trademark Office  
Customer Service Window, Mail Stop Amendment  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314

Sir:

Supplemental to the Information Disclosure Statement submitted to the U.S. Patent and Trademark Office on February 22, 2005, Applicant submits herewith an English language translation of the Japanese office action dated January 14, 2005. The Japanese language office action was provided with the Information Disclosure Statement submitted February 22, 2005.

Again, the undersigned certifies under 37 C.F.R. § 1.97(e)(1) that each reference cited was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

Applicants believe that no fee is necessary pursuant to 37 C.F.R. § 1.97(c). However, if a fee is due, the Office is authorized to charge Deposit Account No. 19-0733.

Respectfully submitted,

Gary D. Fedorochko  
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Date: February 28, 2005

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